

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yeo, et al.

Docket No.:

TSM03-0553

Serial No.:

10/667,871

Art Unit:

2812

Filed:

September 22, 2003

Examiner:

Jennifer M. Kennedy

For:

Resistor With Reduced Leakage

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit:

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Certificate of Mailing via First Class Mail (1 page) Information Disclosure Statement (1 original and 1 copy) Form PTO/SB/08B with 2 references cited (1 page) Copy of 2 references cited Return Postcard

Respectfully submitted,

Kristy Engeldabl

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P. O. Box 1450

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INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08B that may be considered material to the examination of the above-identified application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm. A copy of this sheet is enclosed.

Respectfully submitted,

Date

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6/21/05

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Complete if Known		
Application Number	10/667,871	
Filing Date	September 22, 2003	
First Named Inventor	Yeo, et al.	
Art Unit	2812	
Examiner Name	Jennifer M. Kennedy	
Attorney Docket Number	TSM03-0553	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.	and/or country where published.	T ²
	1	WOLF, S., et al., "Silicon Processing For The VLSI Era," Volume 1: Process Technology, Second Edition, Lattice Press, Sunset Beach, California, 2000, pp. 834-835.	
	2	WOLF, S., "Silicon Processing For The VLSI Era," Volume 2: Process Integration, Lattice Press, Sunset Beach, California, 1990, pp. 144-145.	-

Examiner	Date
Signature	Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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